

PATENT NUMBER

WIDE	PATENT DATE
SCANNED 909 QA 65	

APPLICATION NO. 09/804004	CONT/PRIOR F	CLASS 438	SUBCLASS 740	ART UNIT 2823	EXAMINER N. Fla
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APPLICANTS

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TITLE

Method of manufacturing a semiconductor device

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 TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____	
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